

## 3-3-1 Specifications

## (1) SPH300

Item		Specification			
Type		NP1PS-32/NP1PS-32R	NP1PS-74/NP1PS-74R	NP1PS-117/NP1PS-117R	
Control system		Stored program, Cyclic scanning system (default task), periodic task, event task			
Input / Output connection method		Direct input / output (SX bus), remote input / output (OPCN-1, DeviceNet etc.)			
I/O control system		Via SX bus: Synchronous refresh with Takt Via device level network: Refer to the manual for the corresponding network module.			
CPU		32-bit OS processor, 32-bit execution processor			
Memory types		Program memory, data memory, temporary memory			
Programming language		<When used the D300win> IL language (Instruction List) ST language (Structured Text) LD language (Ladder Diagram) FBD language (Function Block Diagram) SFC elements (Sequential Function Chart)	<When used the Standard Loader> Origin LD language (Ladder Diagram)		
Program memory capacity		32768 steps	75776 steps	119808 steps	
Memory	I/O memory	512 words (Max. 8192 points) Up to 4096 words at I/O extension (65535 points) Note 1)			
	General memory	8192 words	32768 words	131072 words	
	Retain memory	4096 words	16384 words	32768 words	
	User FB instance memory	4096 words	16384 words	32768 words	
	Memory for System FB		16384 words	65536 words	65536 words
		Timer	512 points	2048 points	2048 points
		Integrating timer	128 points	512 points	512 points
		Counter	256 points	1024 points	1024 points
		Edge detection	1024 points	4096 points	4096 points
Others	8192 words	32768 words	32768 words		
System memory	512 words				
No. of tasks		Default tasks (Cyclic scanning): 1 Periodic tasks: 4 Event tasks : 4 (Total of 4 tasks when Periodic task is used)			
No. of programs		Max. 256 (Max. 128 for one task)			
Diagnosis function		Self diagnosis (memory checking, ROM sum checking, CPU basic calculation checking), system configuration monitoring, module fault monitoring			
Secret preserving function		By password (set with the support tool)			
Calendar		Available up to 12/31/2069 23:59:59 Precision: $\pm 27s/month$ (at 25° C, when active) In multi-CPU system the function of correcting the clock setting is provided			
Backup of application program		Flash ROM built in CPU module Backup area: Application program, system definition, ZIP file			
User ROM function (NP1S-32R/74R/117R only)		Application programs, system definitions, zipped files and compressed projects can be stored in user ROM cards.			
Backup of data memory	Backup area	Retain memory, retain attributed memory (e.g. current value of counter), calendar IC memory, RAS area			
	Battery	Lithium primary battery, Replacement time: 5 minutes or less (at 25° C)			
	Backup time	5 years (at 25° C)	When NP8P-BT is used: Approx. 1.3 years (at 25° C) When NP8P-BTS is used: Approx. 3.5 years (at 25° C)		
Occupied slot		1			
Internal current consumption		24V DC 200mA or less	24V DC 200mA or less	24V DC 200mA or less	
Mass		Approx. 200g	Approx. 200g	Approx. 220g (when User ROM card mounted on)	

Note: 1) NP1PS-32/32R is not applied. The maximum memory capacity of NP1PS-74/74R is 1856 words. The maximum memory capacity of NP1PS-117/117R is 4096 words.

(2) SPH300 (NP1PS-245R)

Item		Specification	
Type		NP1PS-245R	
Control system		Stored program, Cyclic scanning system (default task), periodic task, event task	
Input / Output connection method		Direct input / output (SX bus), remote input / output (OPCN-1, DeviceNet etc.)	
I/O control system		Via SX bus: Synchronous refresh with Takt Via device level network: Refer to the manual for the corresponding network module.	
CPU		32-bit OS processor, 32-bit execution processor	
Memory types		Program memory, data memory, temporary memory	
Programming language		<When used the D300win> IL language (Instruction List) ST language (Structured Text) LD language (Ladder Diagram) FBD language (Function Block Diagram) SFC elements (Sequential Function Chart) <When used the Standard Loader> Origin LD language (Ladder Diagram)	
Program memory capacity		250880 steps	
Memory Note 1)	I/O memory	512 words (Max. 8192 points) Up to 4096 words at I/O extension (65535 points)	
	General memory	262144 words	
	Retain memory	130048 words	
	User FB instance memory	66560 words	
	Memory for System FB		65536 words
		Timer	2048 points
		Integrating timer	512 points
		Counter	1024 points
		Edge detection	4096 points
	Others	32768 words	
System memory	512 words		
No. of tasks		Default tasks (Cyclic scanning): 1 Periodic tasks: 4 Event tasks : 4 (Total of 4 tasks when Periodic task is used)	
No. of programs		Max. 256 (Max. 128 for one task)	
Diagnosis function		Self diagnosis (memory checking, ROM sum checking, CPU basic calculation checking), system configuration monitoring, module fault monitoring	
Secret preserving function		By password (set with the support tool)	
Calendar		Available up to 12/31/2069 23:59:59 Precision: ± 27s/month (at 25° C, when active) In multi-CPU system the function of correcting the clock setting is provided	
Backup of application program		Flash ROM built in CPU module Backup area: Application program, system definition, ZIP file	
User ROM function (NP1PS-245R)		Application programs, system definitions, zipped files and compressed projects can be stored in user ROM cards.	
Backup of data memory	Backup area	Retain memory, retain attributed memory (e.g. current value of counter), calendar IC memory, RAS area	
	Battery	Lithium primary battery, Replacement time: 5 minutes or less (at 25° C)	
	Backup time	When NP8P-BT is used: Approx. 0.7 years (at 25° C) When NP8P-BTS is used: Approx. 2 years (at 25° C)	
Occupied slot		1	
Internal current consumption		24V DC 200mA or less	
Mass		Approx. 220g (when User ROM card mounted on)	

## (3) SPH200 (NP1PH-16/08)

Item		Specification	
Type		NP1PH-16	NP1PH-08
Control system		Stored program, Cyclic scanning system (default task), periodic task, event task	
Input / Output connection method		Direct input / output (SX bus), remote input / output (T-link, OPCN-1, DeviceNet etc.)	
I/O control system		Via SX bus: Synchronous refresh with Takt Via device level network: Refer to the manual for the corresponding network module.	
CPU		16-bit OS processor, 16-bit execution processor	
Memory types		Program memory, data memory, temporary	
Programming language		<When used the D300win> IL language (Instruction List) ST language (Structured Text) LD language (Ladder Diagram) FBD language (Function Block Diagram) SFC elements (Sequential Function Chart)	<When used the Standard Loader> Origin LD language (Ladder Diagram)
Program memory capacity		16384 steps	8192 steps
Data memory	I/O memory	512 words (Max. 8192 points) (fixed)	
	Memory for System FB	8192 words (default)	4096 words (default)
		Timer : 256 points (default)	Timer : 128 points (default)
		Integrating timer : 64 points (default)	Integrating timer : 32 points (default)
		Counter : 128 points (default)	Counter : 64 points (default)
		Edge detection : 512 points (default)	Edge detection : 250 points (default)
	Others : 4096 words	Others : 2048 words	
	General memory	8192 words (default)	4096 words (default)
Retain memory	4096 words (default)	2048 words (default)	
User FB memory	4096 words (default)	2048 words (default)	
System memory	512 words (fixed)		
Initial value setting area	7168 words (default)	3072 words (default)	
No. of tasks		Default tasks (Cyclic scanning): 1 Periodic tasks : 4 Event tasks : 4 (Total of 4 tasks when Periodic task is used)	
No. of programs		Max. 64	
Diagnosis function		Self diagnosis (memory checking, ROM sum checking, CPU basic calculation checking), system configuration monitoring, module fault monitoring	
Secret preserving function		By password (set with the support tool)	
Calendar		Available up to 12/31/2069 23:59:59 Precision: ± 27s/month (at 25° C)	
Backup of memory		Battery in CPU module Backup area: Application program, system definition, ZIP file, retain memory, retain attributed memory (e.g. current value of counter), calendar IC memory, RAS area Battery: Lithium primary battery Backup time: 5 years (at 25° C) Replacement time: 5 minutes or less (at 25° C)	
Occupied slot		1	
Internal current consumption		24V DC 85mA or less	
Mass		Approx. 170g	

## (4) SPH2000 (NP1PM-48R/48E/256E/256H)

Item	Specification			
Type	NP1PM-48R/48E	NP1PM-256E/256H		
Control system	Stored program, Cyclic scanning system (default task), periodic task, event task			
Input / Output connection method	Direct input / output (SX bus), remote input / output (OPCN-1, DeviceNet etc.)			
I/O control system	Via SX bus: Synchronous refresh with Takt Via device level network: Refer to the manual for the corresponding network module.			
CPU	32-bit RISC processor			
Memory types	Program memory, data memory, temporary memory			
Programming language	<When used the D300win> IL language (Instruction List Diagram) ST language (Structured Text) LD language (Ladder Diagram) FBD language (Function Block Diagram) SFC elements (Sequential Function Chart)	<When used the Standard Loader> Origin LD language (Ladder Diagram)		
Program memory capacity	49152 steps	262144 steps		
Memory	I/O memory	512 words (Max. 8192 points)		
	General memory	65536 words	1703936 words	
	Retain memory	8192 words	262144 words	
	User FB instance memory	8192 words	65536 words	
	Memory for System FB		16384 words	65536 words
		Timer	512 points	2048 points
		Integrating timer	128 points	512 points
		Counter	256 points	1024 points
		Edge detection	1024 points	4096 points
		Others	8192 words	32768 words
System memory	512 words			
No. of tasks	Default tasks (Cyclic scanning): 1 *The default task has the constant scan function. Periodic tasks: 4 Event tasks : 4 (Total of 4 tasks when Periodic task is used)			
No. of programs	Max. 256 (Max. 128 for one task)			
Diagnosis function	Self diagnosis (memory checking, ROM sum checking, system configuration monitoring, module fault monitoring)			
Secret preserving function	By password (set with the support tool)			
Calendar	Available up to 12/31/2069 23:59:59 Precision: ± 27s/month (at 25° C, when active) In multi-CPU system the function of correcting the clock setting is provided			
Backup of application program	Flash ROM built in CPU module Backup area: Application program, system definition, ZIP file			
User ROM function	Application programs, system definitions, zipped files and compressed projects can be stored in user ROM cards.			
Backup of data memory	Backup area	Retain memory, retain attributed memory (e.g. current value of counter), calendar IC memory, RAS area		
	Battery	Lithium primary battery, Replacement time: 5 minutes or less (at 25° C)		
	Backup time	5 years (at 25° C)		
Occupied slot	1			
Internal current consumption	24V DC 200mA or less			
Mass	Approx. 220g (when User ROM card mounted on)			

Note: Ethernet connector of NP1PM-256H is used as a bus for data equalization in a redundant system configuration. Thus, Ethernet communication cannot be executed with NP1PM-256H.

## (5) SPH3000 (NP1PU-048E/256E)

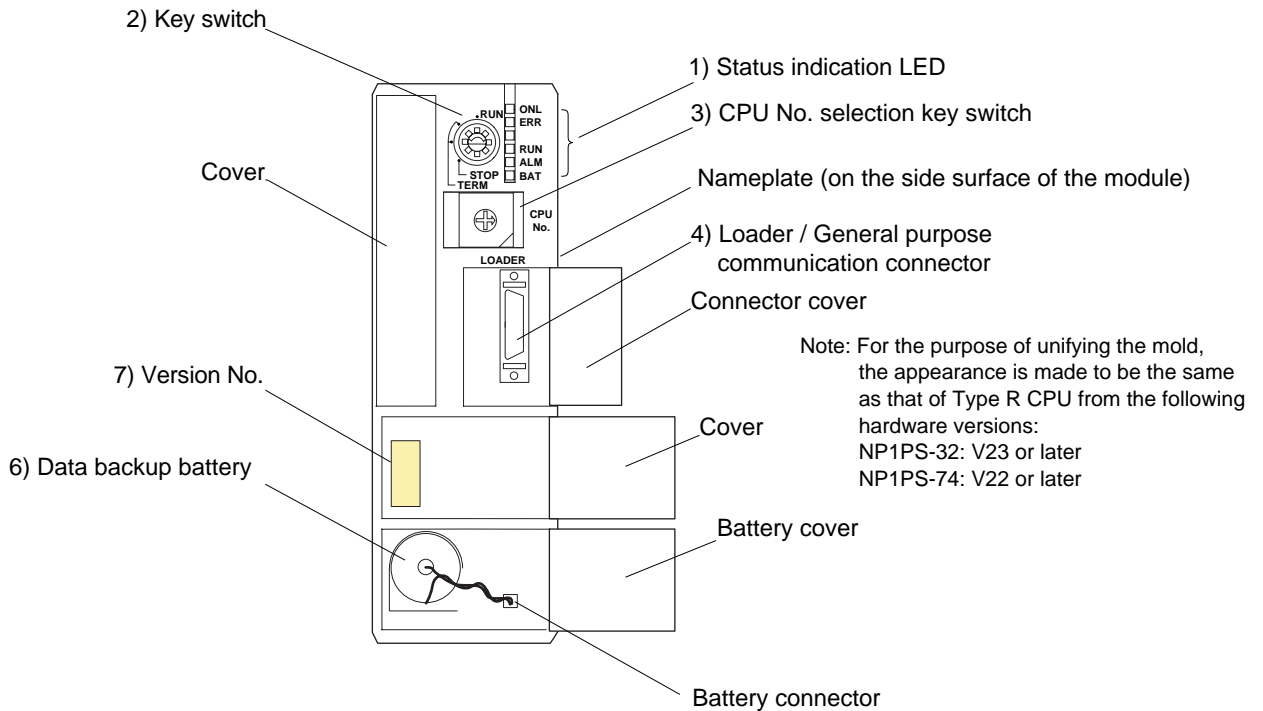
Item		Specification		
Type		NP1PU-048E	NP1PU-256E	
Control system		Stored program, Cyclic scanning system (default task), periodic task, event task		
Input / Output connection method		Direct input / output (SX bus), remote input / output (OPCN-1, DeviceNet etc.)		
I/O control system		Via SX bus: Synchronous refresh with Takt Via device level network: Refer to the manual for the corresponding network module.		
CPU		32-bit RISC processor		
Memory types		Program memory, data memory, temporary memory		
Programming language		<When used the D300win> IL language (Instruction List) ST language (Structured Text) LD language (Ladder Diagram) FBD language (Function Block Diagram) SFC elements (Sequential Function Chart)		
Program memory capacity		49152 steps	262144 steps	
Memory	I/O memory	512 words (Max. 8192 points)		
	General memory	98304 words (default value)	1703936 words (default value)	
	Retain memory	40960 words (default value)	237568 words (default value)	
	User FB instance memory	40960 words (default value)	73728 words (default value)	
	Memory for System FB		81920 words (default value)	81920 words (default value)
		Timer	2560 points	2560 points
		Integrating timer	640 points	640 points
		Counter	1280 points	1280 points
		Edge detection	5120 points	5120 points
		Others	40960 words	40960 words
System memory	512 words			
No. of tasks		Default tasks (Cyclic scanning): 1 *The default task has the constant scan function. Periodic tasks: 4 Event tasks : 4 (Total of 4 tasks when Periodic task is used)		
No. of programs		Max. 256 (Max. 128 for one task)		
Diagnosis function		Self diagnosis (memory checking, ROM sum checking, system configuration monitoring, module fault monitoring)		
Secret preserving function		By password (set with the support tool)		
Calendar		Available up to 12/31/2069 23:59:59 Precision: $\pm 27$ s/month (at 25°C, when active) In multi-CPU system the function of correcting the clock setting is provided		
Backup of application program		Flash ROM built in CPU module Backup area: Application program, system definition, ZIP file		
User ROM function		Application programs, system definitions, zipped files and compressed projects can be stored in user ROM cards.		
Backup of data memory	Backup area	Retain memory, retain attributed memory (e.g. current value of counter), calendar IC memory, RAS area		
	Battery	Lithium primary battery, Replacement time: 5 minutes or less (at 25°C)		
	Backup time	5 years (at 25°C)		
Occupied slot		1		
Internal current consumption		24V DC 200mA or less		
Mass		Approx. 220g (when User ROM card mounted on)		

## (6) SPH300EX (NP1PS-74D)

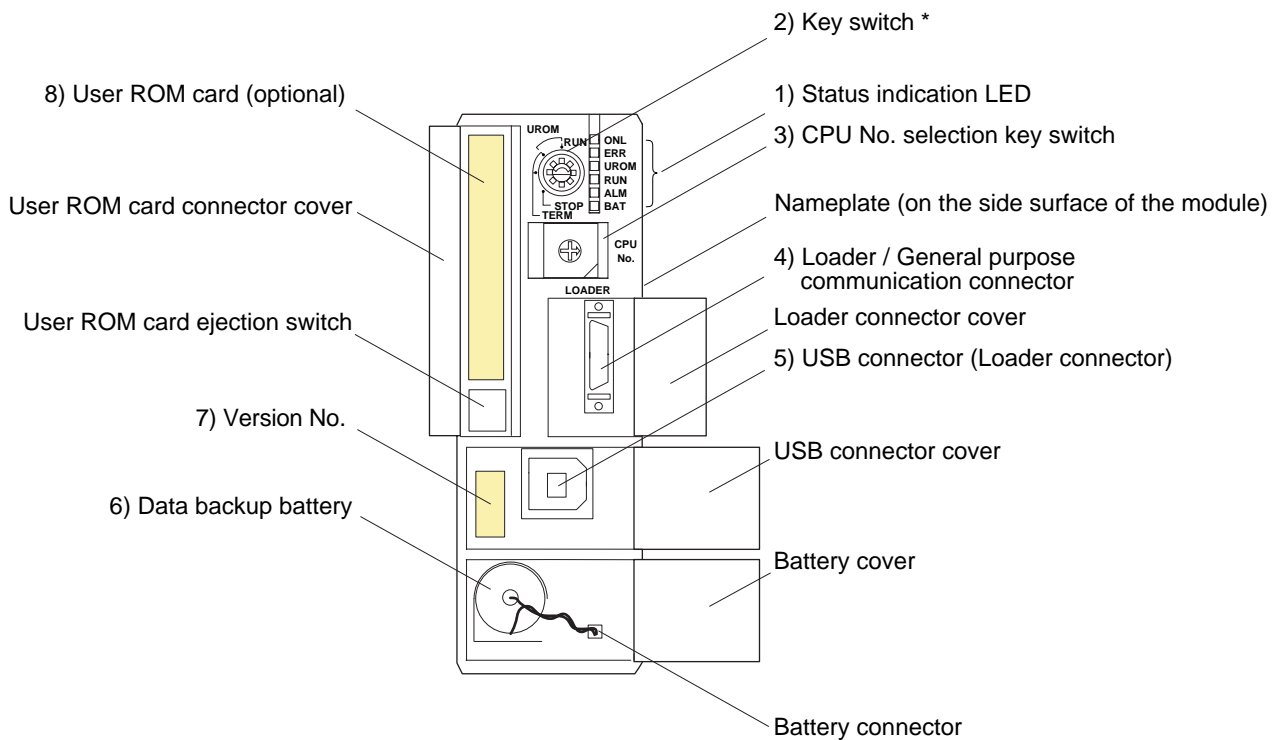
Item	Specification		
Type	NP1PS-74D		
Control system	Stored program, Cyclic scanning system (default task), periodic task, event task		
Input / Output connection method	Direct input / output (SX bus), remote input / output (OPCN-1, DeviceNet etc.)		
I/O control system	Via SX bus: Synchronous refresh with Takt Via device level network: Refer to the manual for the corresponding network module.		
CPU	32-bit OS processor, 32-bit execution processor		
Memory types	Program memory, data memory, temporary memory		
Programming language	<When used the D300win> IL language (Instruction List) ST language (Structured Text) LD language (Ladder Diagram) FBD language (Function Block Diagram) SFC elements (Sequential Function Chart) <When used the Standard Loader> Origin LD language (Ladder Diagram)		
Program memory capacity	75776 steps x 2		
Memory	I/O memory	512 words (Max. 8192 points) Up to 1856 words at I/O extension (29696 points)	
	General memory	32768 words x 2	
	Retain memory	16384 words x 2	
	User FB instance memory	16384 words x 2	
	Memory for System FB		65536 words x 2
		Timer	2048 points x 2
		Integrating timer	512 points x 2
		Counter	1024 points x 2
Edge detection		4096 points x 2	
Others	32768 words x 2		
System memory	512 words x 2		
No. of tasks	Default tasks (Cyclic scanning): 1 Periodic tasks: 4 Event tasks : 4 (Total of 4 tasks when Periodic task is used)		
No. of programs	Max. 256 (Max. 128 for one task)		
Diagnosis function	Self diagnosis (memory checking, ROM sum checking, CPU basic calculation checking), system configuration monitoring, module fault monitoring		
Secret preserving function	By password (set with the support tool)		
Calendar	Available up to 12/31/2069 23:59:59 Precision: ± 27s/month (at 25° C, when active) In multi-CPU system the function of correcting the clock setting is provided		
Backup of application program	Flash ROM built in CPU module Backup area: Application program, system definition, ZIP file		
User ROM function (NP1S-32R/74R/117R only)	Application programs, system definitions, zipped files and compressed projects can be stored in user ROM cards.		
Backup of data memory	Backup area	Retain memory, retain attributed memory (e.g. current value of counter), calendar IC memory, RAS area	
	Battery	Lithium primary battery, Replacement time: 5 minutes or less (at 25° C)	
	Backup time	When NP8P-BT is used: Approx. 0.65 years (at 25° C) When NP8P-BTS is used: Approx. 1.75 years (at 25° C)	
Occupied slot	2		
Internal current consumption	24V DC 200mA or less		
Mass	Approx. 410g		

3-3-2 Names and functions

(1) SPH300 NP1PS-32/NP1PS-74/NP1PS-117



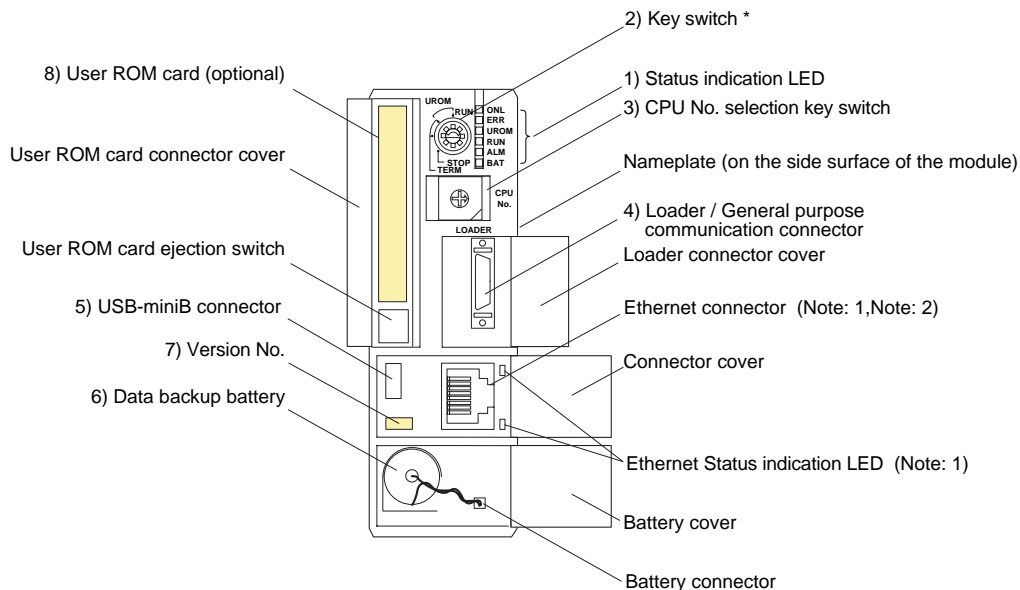
(2) SPH300 (User ROM card adapted CPU) NP1PS-32R/74R/117R/245R



\* For more information about the operation of the CPU with key switches, refer to "Appendix 1 Operation of the high-performance CPU with key switches", in addition to the explanation on the following pages.



### (3) SPH2000 NP1PM-48R, NP1PM-48E/256E/256H (with Ethernet interface)



Note: 1) The Ethernet communication function is supported only in NP1PM-48E/256E.

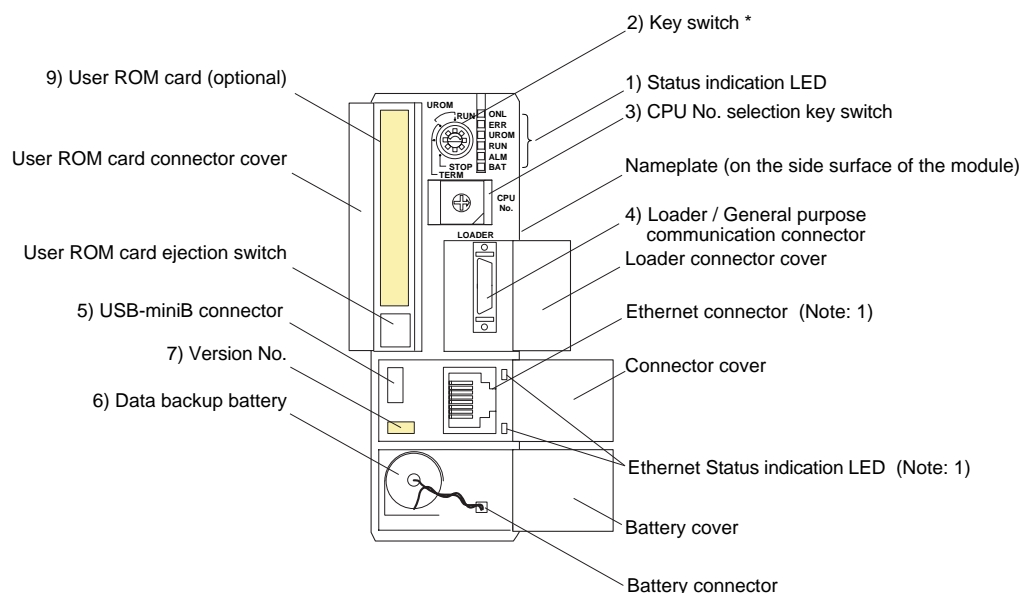
For details on specifications and handling, see "SPH2000/3000 Ethernet Communication Edition (FEH193)."

#### Ethernet status indication LED

LED position	Ethernet status
Upper side	LINK status. Lights on when LAN cable is connected to external device (such as HUB).
Lower side	TX/RX status. Blinks when communication.

2) Ethernet connector of NP1PM-256H is used as part of a bus for inputting/outputting equivalent data in a redundant system configuration. Thus, the connector cannot be used for Ethernet communication. For details of SPH2000 redundant system configurations, refer to "SPH2000 Redundant Function Edition (FEH184)".

### (4) SPH3000 NP1PU-048E/256E



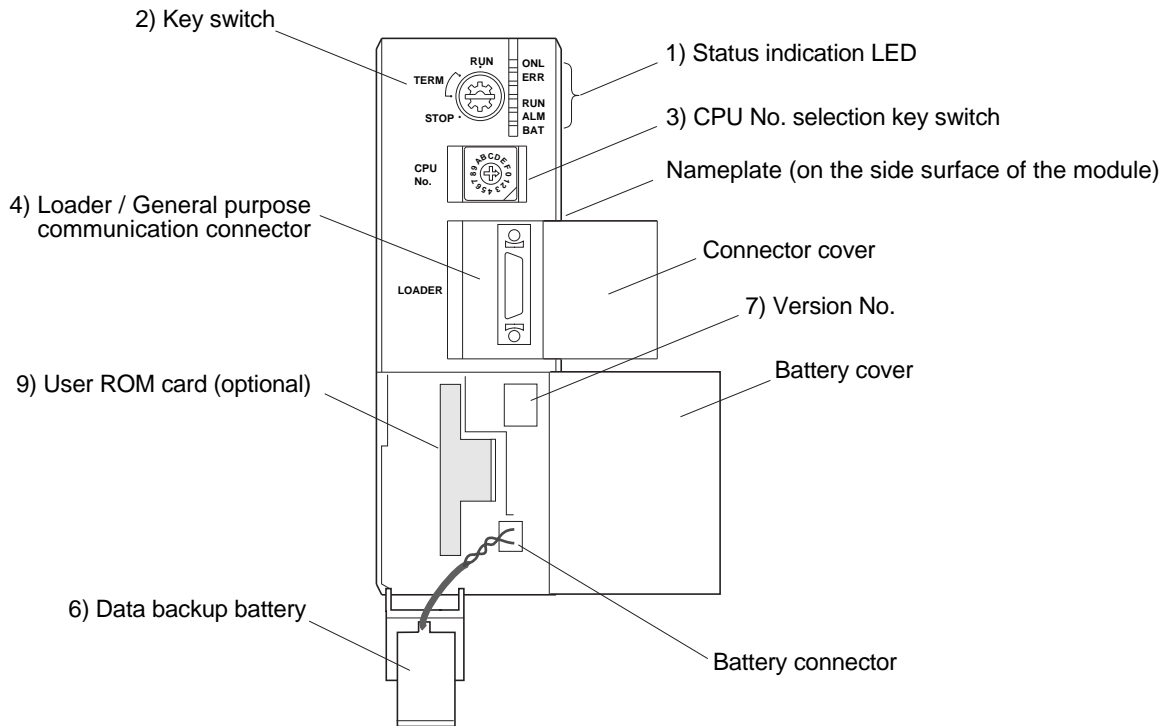
Note: 1) For details on specifications and handling, see "SPH2000/3000 Ethernet Communication Edition (FEH193)."

#### Ethernet status indication LED

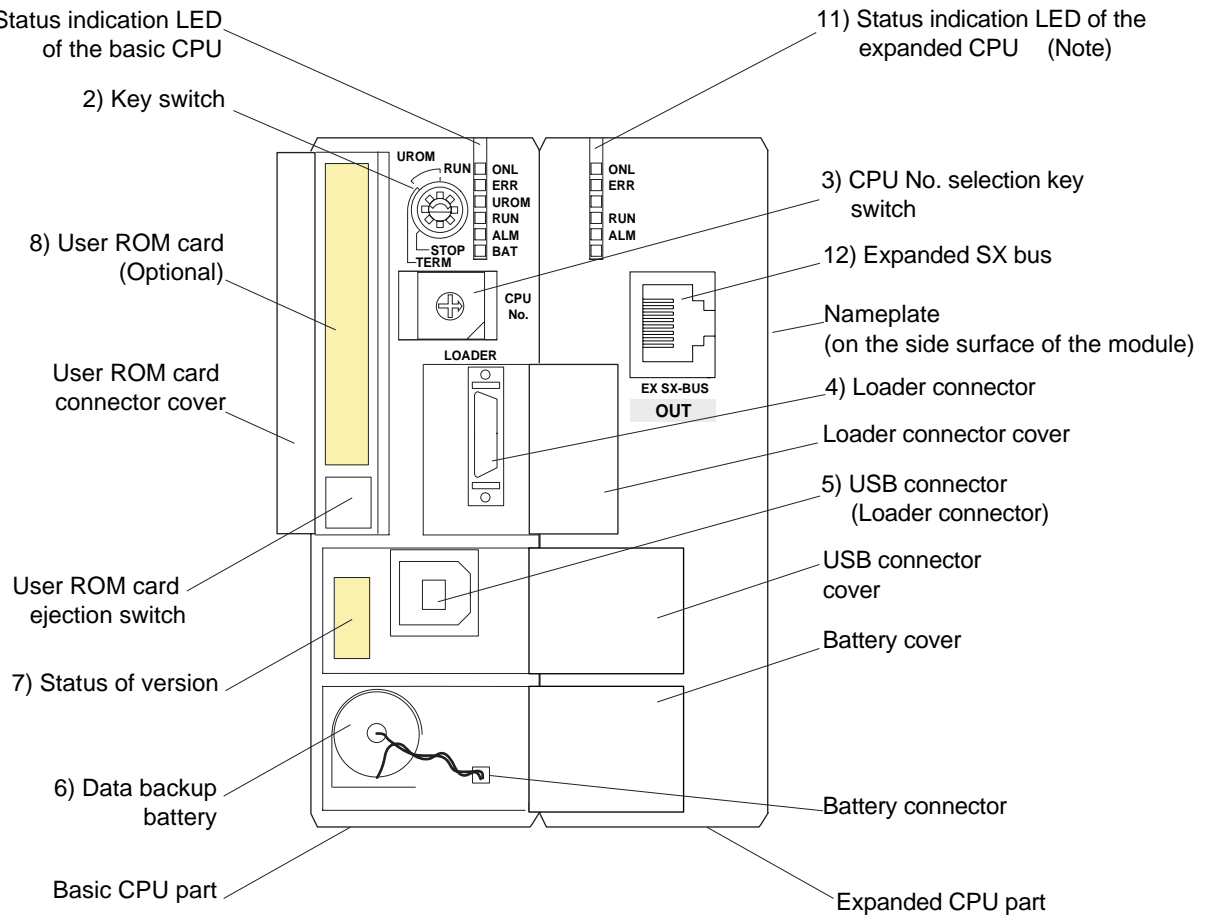
LED position	Ethernet status
Upper side	LINK status. Lights on when LAN cable is connected to external device (such as HUB).
Lower side	TX/RX status. Blinks when communication.



(5) SPH200 NP1PH-16/NP1PH-08



(6) SPH300EX NP1PS-74D



\* For specification and operation of the SPH300EX, refer to the "User's Manual SPH300EX" (FEH192).

## 1) Status indication LED

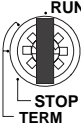


Symbol	Color	Description																		
ONL ERR	Green Red	Status of own CPU module  <Lights on pattern> <table border="1"> <tr> <td>ONL</td> <td>ERR</td> <td>Status of own CPU module</td> </tr> <tr> <td>OFF</td> <td>OFF</td> <td>Power OFF, system resetting or initializing</td> </tr> <tr> <td>Blinks</td> <td>-</td> <td>SX bus standing on</td> </tr> <tr> <td>ON</td> <td>OFF</td> <td>Normally running (initial diagnosis completed)</td> </tr> <tr> <td>ON</td> <td>ON</td> <td>Nonfatal fault, at a running</td> </tr> <tr> <td>OFF</td> <td>ON</td> <td>Fatal fault at a stop</td> </tr> </table>	ONL	ERR	Status of own CPU module	OFF	OFF	Power OFF, system resetting or initializing	Blinks	-	SX bus standing on	ON	OFF	Normally running (initial diagnosis completed)	ON	ON	Nonfatal fault, at a running	OFF	ON	Fatal fault at a stop
ONL	ERR	Status of own CPU module																		
OFF	OFF	Power OFF, system resetting or initializing																		
Blinks	-	SX bus standing on																		
ON	OFF	Normally running (initial diagnosis completed)																		
ON	ON	Nonfatal fault, at a running																		
OFF	ON	Fatal fault at a stop																		
UROM	Green	Lights on continuously when the CPU recognizes a user ROM card. Lights on continuously when a user ROM card (compact flash card) is correctly installed in the CPU module and the key switch is set to UROM. (User ROM card adapted high-performance CPU, SPH 2000/3000 only)																		
RUN ALM	Green Red	Status of system (Note)  <Lights on pattern> <table border="1"> <tr> <td>RUN</td> <td>ALM</td> <td>Status of system</td> </tr> <tr> <td>OFF</td> <td>OFF</td> <td>Power OFF or application program at a stop</td> </tr> <tr> <td>ON</td> <td>OFF</td> <td>Normally running</td> </tr> <tr> <td>ON</td> <td>ON</td> <td>Nonfatal fault, at a running</td> </tr> <tr> <td>OFF</td> <td>ON</td> <td>Fatal fault, at a stop</td> </tr> <tr> <td>Blinks</td> <td>-</td> <td>While the CPU is accessing the user ROM</td> </tr> </table>	RUN	ALM	Status of system	OFF	OFF	Power OFF or application program at a stop	ON	OFF	Normally running	ON	ON	Nonfatal fault, at a running	OFF	ON	Fatal fault, at a stop	Blinks	-	While the CPU is accessing the user ROM
RUN	ALM	Status of system																		
OFF	OFF	Power OFF or application program at a stop																		
ON	OFF	Normally running																		
ON	ON	Nonfatal fault, at a running																		
OFF	ON	Fatal fault, at a stop																		
Blinks	-	While the CPU is accessing the user ROM																		
BAT	Orange	Turned on when data backup battery dropped or disconnected.																		

Note: The system includes the own CPU.

## 2) Key switch

How the CPU operates when individual key switch is set at various positions is described below.

### <Other than user ROM card adapted high-performance CPU,SPH2000/3000>

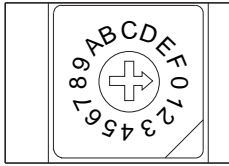
Position of key	Operation of CPU
RUN 	<ul style="list-style-type: none"> <li>When this switch is changed over from STOP or TERM to RUN, the CPU module starts operation.</li> <li>If this switch is at the RUN position when powered on, the CPU starts running.</li> <li>* Monitoring and reading from loader are possible. (Data can be read or written.)</li> </ul>
TERM 	<ul style="list-style-type: none"> <li>The previous condition is kept.</li> <li>When this switch is changed over from STOP to TERM, the CPU module continues to be stopped.</li> <li>When this switch is changed over from RUN to TERM, the CPU module continues to run.</li> <li>If the system is powered on when this switch is at the TERM position, the CPU operates according to the setting of [Running specification at power on] on the [CPU running definition] tab window.</li> <li>• TERM = Run (default) ⇒ Operation is started.</li> <li>• TERM = Last State, ⇒ When the power was turned off in running condition last time, operation is started. When the power was turned off in stop condition last time, operation is not started.</li> <li>• TERM = Stop ⇒ Operation is not started.</li> <li>* Monitoring, reading and writing from loader are possible.</li> </ul>
STOP 	<ul style="list-style-type: none"> <li>When this switch is changed over from RUN or TERM to STOP, the CPU module stops.</li> <li>If this switch is at the STOP position when powered on, the CPU stops. Operation is not started.</li> <li>* Monitoring and reading from loader are possible. (Data can be read or written.)</li> </ul>

### <User ROM card adapted high-performance CPU,SPH 2000/3000>

- \* For more information about the operation of CPU, refer to "Appendix 1 Operation of the user ROM card adapted CPU with Key Switches".

**3) CPU No. selection key switch**

This switch is used to select the CPU number. Be sure to select "0" for a one-CPU system. CPU number is selected in order from "0" for the multi-CPU system.



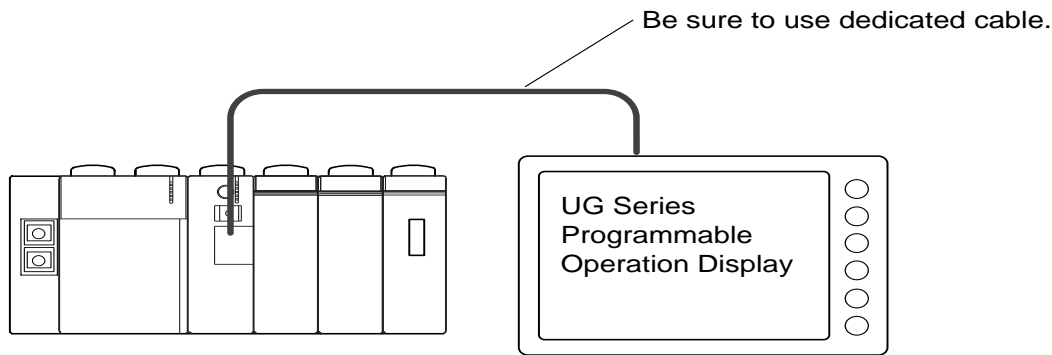
Note: Do not change during operation, otherwise, system stop might be caused.

**4) Loader / General purpose communication connector**

Used to connect a program loader.

**[Note: ]**

It is possible to connect FUJI UG Series Programmable Operation Display to the program loader connector.

**5) USB / USB-miniB connector (program loader connector)**

Used to connect a program loader. Use commercially available USB cable.

Note: USB (Universal Serial Bus) is a standard for external peripheral devices for personal computer. When you use a USB cable, be careful of the following matters, taking into consideration the noise immunity of the personal computer to be connected.

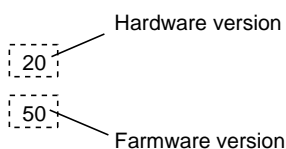
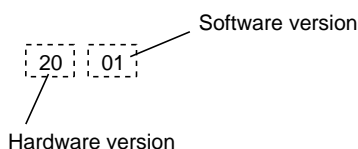
- USB cable should be separated from power line as far as possible.

**6) Data backup battery**

The battery backs up the retain memory, the calendar data and so on in the CPU module. Backup time: 5 years (at 25°C).

**7) Version No.**

Version No. of the CPU module is described.

**SPH300/SPH200/SPH300EX****SPH2000/3000****8) User ROM card (compact flash card)**

Application programs, system definitions, zipped files and compressed projects can be saved in user ROM cards. (User ROM card is optional)

For more information about the specification and handling of user ROM card, refer to 3-3-3 (Specification of user ROM card (compact flash card)).

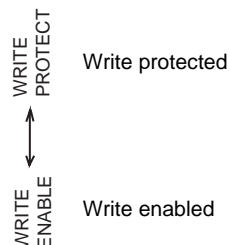
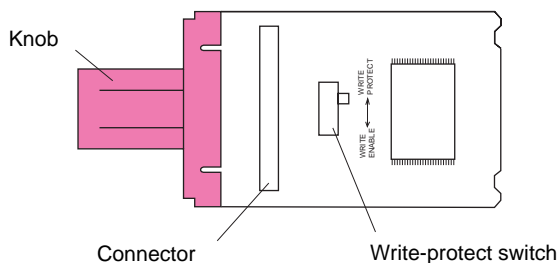
**9) User ROM card (SD card)**

Application programs, system definitions, zipped files and compressed projects can be saved in user ROM cards. (User ROM card is optional)

For more information about the specification and handling of user ROM card, refer to 3-3-4 (Specification of user ROM card (SD card)).

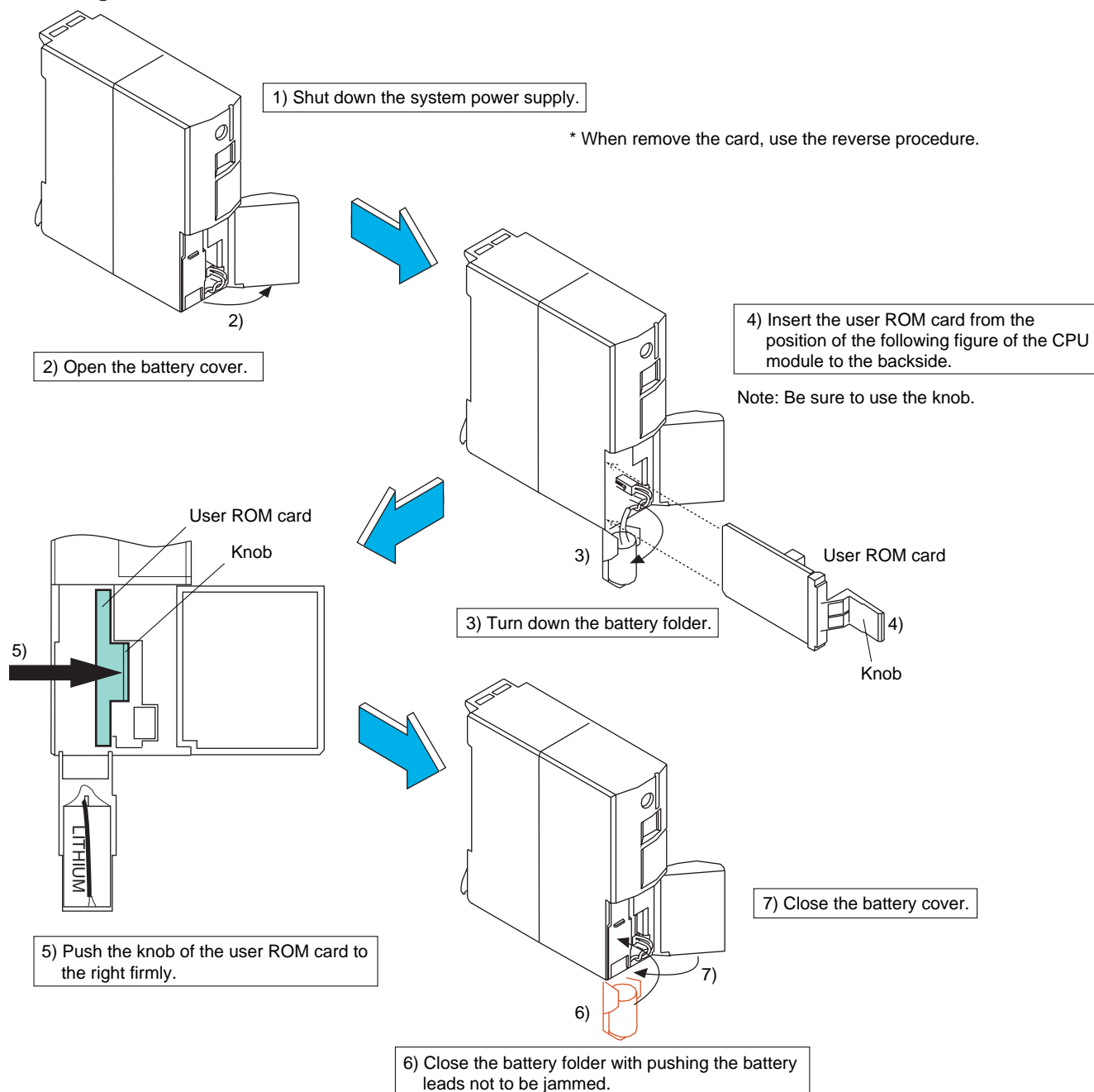
## 10) User ROM card NP8PMF-16 (optional)

This card is used to store application program, system definition and ZIP file of a standard CPU module into the flash ROM. Mounting this card enables battery-less operation of standard CPU.



Note: Set to "Protected" when program, system definition and ZIP file are not rewritten.

### <Mounting the user ROM card>



11) Status indication LED of the expanded CPU

Symbol	Color	Description
ONL ERR	Green Red	Status of expanded CPU module
		<Lights on pattern>
		ONL ERR Status of own CPU module
		OFF OFF Power OFF, system resetting or initializing
		Blinks - SX bus standing on
		ON OFF Normally running (initial diagnosis completed)
		ON ON Nonfatal fault, at a running
		OFF ON Fatal fault at a stop
RUN ALM	Green Red	Status of system (Note)
		<Lights on pattern>
		RUN ALM Status of system
		OFF OFF Power OFF or application program at a stop
		ON OFF Normally running
		ON ON Nonfatal fault, at a running
		OFF ON Fatal fault, at a stop

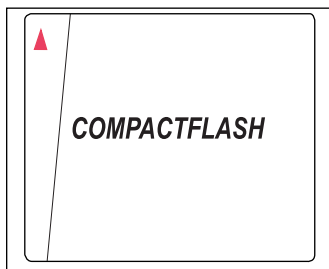
Note: The system includes the basic and expanded CPU.

11) SX bus expansion

This bus is specifically for applications that require high-speed I/O response. The devices that can be connected to the bus are limited to I/O modules that are compatible with the SX bus and servos and inverters conforming to these I/O modules.

## 3-3-3 Specification of user ROM card (compact flash card)

### (1) Appearance and specification



#### <Specification of user ROM card>

Type (maker)	NP8PCF-256
Standard	Compact flash ATA card, Type I CFA 3.0
Specification of card	Industrial grade Operating temperature: -25 to 85 Number of re-write: more than 100 thousands
Memory capacity	256MB

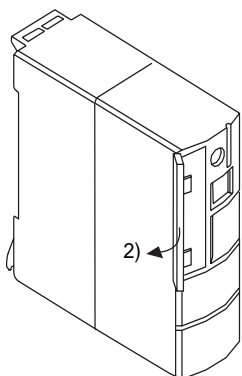
Note: 1 When you install a user ROM card in the loader and directly access it, use commercially available compact flash adapter for PCMCIA card slot or commercially available compact flash reader/writer.

Note: 2 Format the user ROM card in "FAT."  
In case of SPH2000, "FAT" or "FAT32".

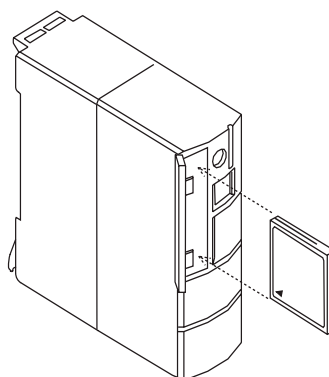
### (2) User ROM card mounting procedure

\* For removal, do in the reverse order.

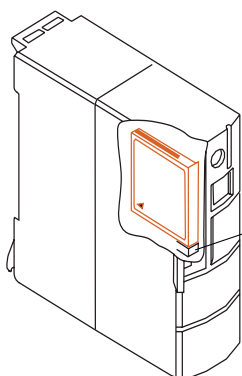
1) Shut down the system power supply or set the key switch to a position other than UROM. (The UROM LED turned off.)



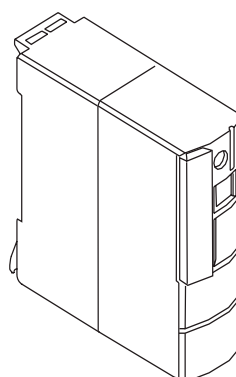
2) Open the user ROM card connector cover.



3) Insert a user ROM card in the slot of the CPU module, according to the above figure.



4) When a user ROM card is inserted, the user ROM card eject button is protruded. (Press this button to take the card out.)



5) Close the user ROM card connector cover.

6) Turn the system power supply ON and set the key switch to UROM, and the CPU module will recognize the user ROM card.

## 3-3 CPU module Specifications

### (3) Operation of the CPU when it recognizes a user ROM card

State of user ROM card	When CPU stops	When CPU is running	When CPU starts to run from stop condition
1) Initialized, no run project	Clears the flash memory in the CPU in which run project is stored.	Outputs nonfatal fault signal and sets mismatch flag ON. (Note 1)	Clears the internal flash memory of the CPU in which run project is stored.
2) Run project normal (the content of CPU internal flash memory does not match that of the user ROM card) (Note 5)	Updates the CPU internal flash memory (by copying the content of the user ROM card).	Outputs nonfatal fault signal and sets mismatch flag ON.	Updates the CPU internal flash memory (by copying the content of the user ROM card).
3) Run project error (user ROM card not initialized, file destroyed, etc.) (Note 2, Note 6)	CPU comes in fatal fault condition.	Outputs fatal fault signal and sets mismatch flag ON.	CPU comes in fatal fault condition.

Notes:1) Mismatch flag is %MX10.4.12. Nonfatal fault does not occur when no run project exists in the CPU, either.

2) The CPU detects physical error when it can recognize the user ROM card but can read no sector data.

When the CPU cannot recognize the user ROM card, it operates the same as no card being set.

3) To initialize the user ROM card, insert the card in the CPU module and execute "Resource initialization," or execute "Initialize" in the memory card menu of loader.

4) Execution of "Resource initialization" with a user ROM card mounted in the CPU module cannot initialize the user ROM card in DOS format. When DOS format is destroyed, execute offline initialization.

5) When the power switch of the CPU module is turned on, it may take approximately 3 minutes to copy the content of the user ROM card in the internal flash memory of the CPU. Multi-CPU system recognizes this condition as the copying CPU being disconnected by other CPU. In such case, turn the system on again after the RUN LED of the copying CPU stops blinking.

6) Be sure to initialize (FAT format) the user ROM card before you use it.

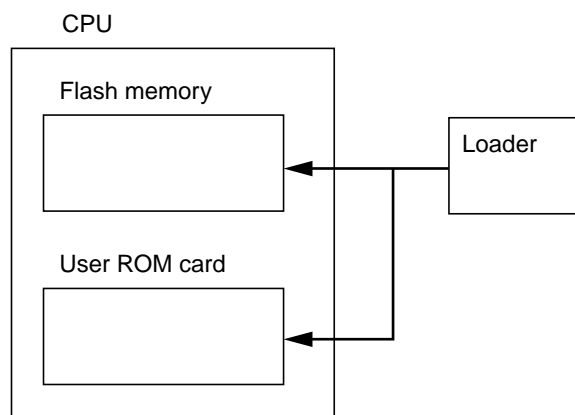
### (4) Timing to transfer data from user ROM card to CPU internal flash memory

When transferring data (application program, system definition, ZIP file) from the user ROM card installed in the CPU module to the CPU internal flash memory under the following conditions, the content of the user ROM card is compared with that of the CPU internal flash memory and the data is transferred if the comparison results in mismatch.

- When the power switch of the CPU module is turned ON with the key switch set at UROM
- When resetting is performed from loader with the key switch set at UROM
- When the key switch is changed over to UROM from a position other than UROM while the CPU stops.

### (5) Downloading from the user ROM card mounted in the CPU module

To download data from loader to the user ROM card mounted in the CPU module, set the key switch to UROM\_TERM (the UROM LED lights up) and execute the downloading from loader to the CPU. Then, the data is downloaded to the internal flash memory of the CPU as well as to the user ROM card at a time.



Notes: 1) When unformatted or write protected user ROM card is mounted in the CPU module, no data is downloaded to the flash memory or the user ROM card.

Notes: 2) It is possible to insert the user ROM card in the personal computer in which Loader is installed and to download the data.

For the operating method, refer to the "User's Manual Loader <Reference>."



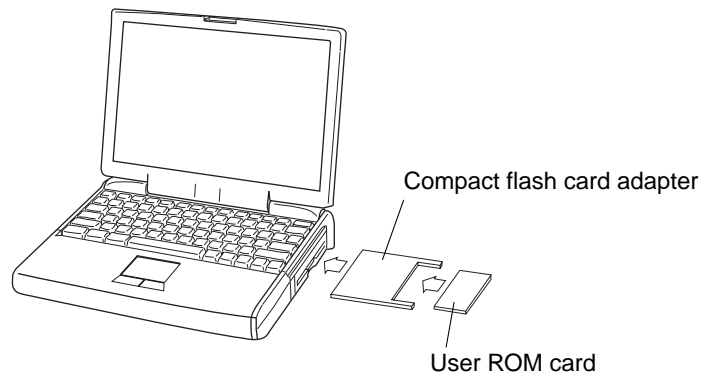
## 3-3 CPU module Specifications

### (6) How to initialize the user ROM card

#### 1) Offline initialization

Insert the user ROM card in the compact flash card adapter or compact flash card reader/writer to make the personal computer recognize the card. For initialization, "Memory card utility" in the "Extras" menu of loader is used. The utility executes both DOS formatting and initialization (creation of directories and files).

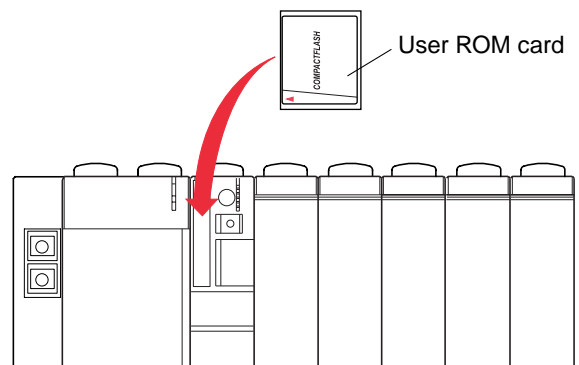
Note: Perform the DOS formatting in "FAT."



#### 2) Initialization by mounting a user ROM card in the CPU module

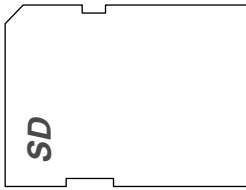
When you initialize a user ROM card that is mounted in the CPU module, be careful that only DOS formatted cards can be initialized (to create directories and files).

Insert a user ROM card in the CPU module and execute "Resource initialization" from loader when the CPU recognizes the user ROM card.



3-3-4 Specification of user ROM card (SD card)

(1) Appearance and specification



 SDHC logo is trademark

<Specification of user ROM card>

Item	Contents	
PLC card slot specification	Standard	SD Physical layer Specification Version 2.00
	Supported memory card	SD memory card, SDHC memory card
	Specification	Power supply: 3.3V ± 0.3V, Max 100mA
ROM card specification	Type	NP8PSD-002
	Standard	SD card spec V1.1
	Card specification	Industrial grade Operating Temperature: -40 to 85
	Memory capacity	2GB

Note: 1 When you install a user ROM card in the loader and directly access it, use commercially available SD card adapter for PCMCIA card slot or commercially available SD card reader/writer.

Note: 2 Format the user ROM card in "FAT" or "FAT32".

Note: 3 Number of re-write of user ROM.

Number of re-write varies depends on writing data size.

Below table shows Fuji user ROM card (Type:NP8PSD-002) as reference.

Write data size	Number of re-write	Note
1MB	2 millions	Max writing data size by application program is 1MB
20MB	100 thousands	Download project size may exceed 1MB depends on program

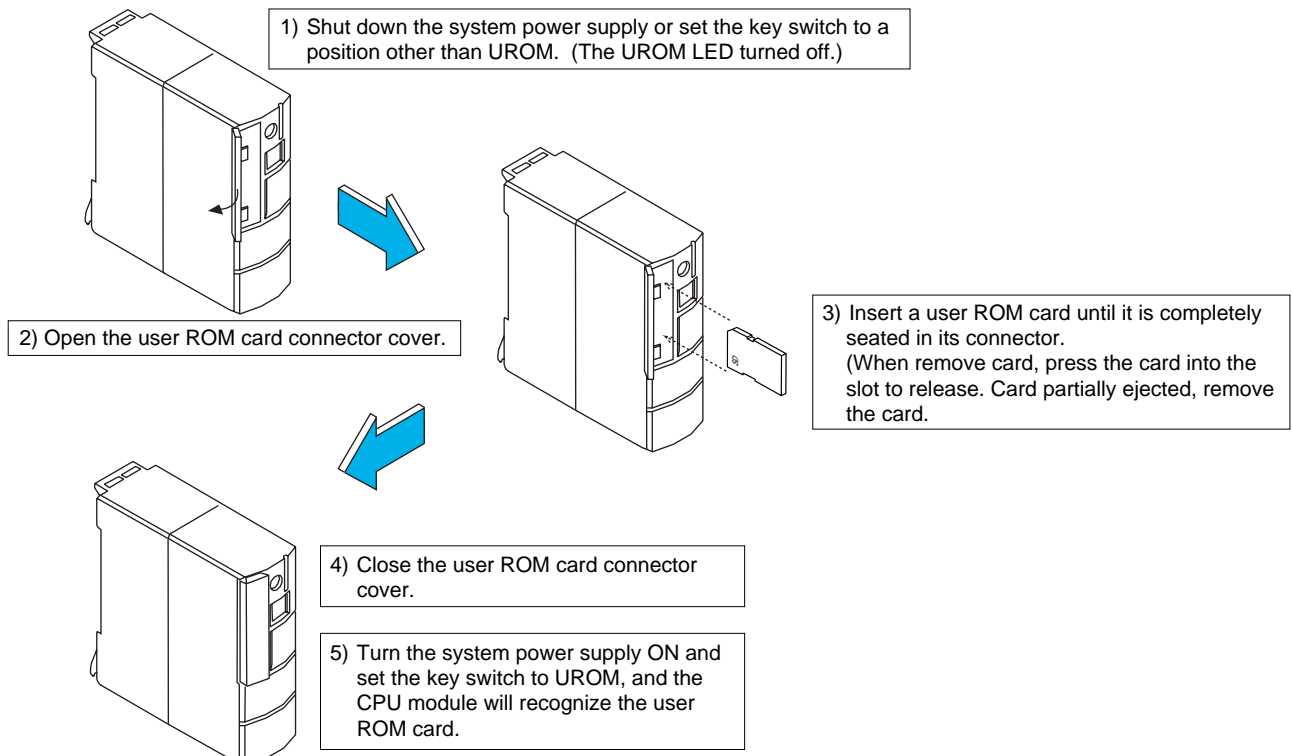
Note: 4 We recommend to use Fuji user ROM card.

If you use market sale SD card, check operation by yourself. In this case, do not use mini SD/ micro SD with SD adapter.

Make sure to use SD card.

(2) User ROM card mounting procedure

\* For removal, do in the reverse order.



### (3) Operation of the CPU when it recognizes a user ROM card

State of user ROM card	When CPU stops	When CPU is running	When CPU starts to run from stop condition
1) Initialized, no run project	Clears the flash memory in the CPU in which run project is stored.	Outputs nonfatal fault signal and sets mismatch flag ON. (Note 1)	Clears the internal flash memory of the CPU in which run project is stored.
2) Run project normal (the content of CPU internal flash memory does not match that of the user ROM card) (Note 5)	Updates the CPU internal flash memory (by copying the content of the user ROM card).	Outputs nonfatal fault signal and sets mismatch flag ON.	Updates the CPU internal flash memory (by copying the content of the user ROM card).
3) Run project error (user ROM card not initialized, file destroyed, etc.) (Note 2, Note 6)	CPU comes in fatal fault condition.	Outputs fatal fault signal and sets mismatch flag ON.	CPU comes in fatal fault condition.

- Notes:1) Mismatch flag is %MX10.4.12. Nonfatal fault does not occur when no run project exists in the CPU, either.  
 2) The CPU detects physical error when it can recognize the user ROM card but can read no sector data. When the CPU cannot recognize the user ROM card, it operates the same as no card being set.  
 3) To initialize the user ROM card, insert the card in the CPU module and execute "Resource initialization," or execute "Initialize" in the memory card menu of loader.  
 4) Execution of "Resource initialization" with a user ROM card mounted in the CPU module cannot initialize the user ROM card in DOS format. When DOS format is destroyed, execute offline initialization (Format card by "FAT" or "FAT32").  
 5) When the power switch of the CPU module is turned on, it may take approximately 3 minutes to copy the content of the user ROM card in the internal flash memory of the CPU. Multi-CPU system recognizes this condition as the copying CPU being disconnected by other CPU. In such case, turn the system on again after the RUN LED of the copying CPU stops blinking.  
 6) Be sure to initialize ("FAT" or "FAT32" format) the user ROM card before you use it.

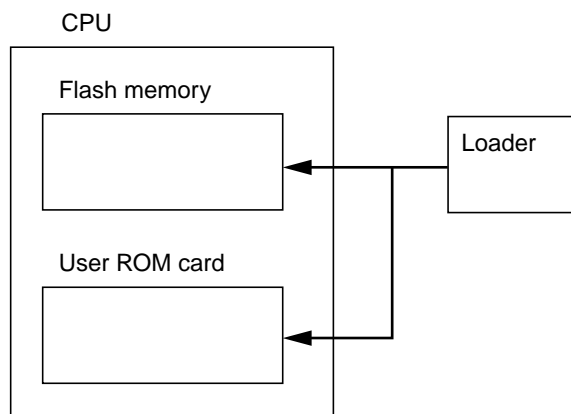
### (4) Timing to transfer data from user ROM card to CPU internal flash memory

When transferring data (application program, system definition, ZIP file) from the user ROM card installed in the CPU module to the CPU internal flash memory under the following conditions, the content of the user ROM card is compared with that of the CPU internal flash memory and the data is transferred if the comparison results in mismatch.

- When the power switch of the CPU module is turned ON with the key switch set at UROM
- When resetting is performed from loader with the key switch set at UROM
- When the key switch is changed over to UROM from a position other than UROM while the CPU stops.

### (5) Downloading from the user ROM card mounted in the CPU module

To download data from loader to the user ROM card mounted in the CPU module, set the key switch to UROM\_TERM (the UROM LED lights up) and execute the downloading from loader to the CPU. Then, the data is downloaded to the internal flash memory of the CPU as well as to the user ROM card at a time.



- Notes: 1) When unformatted or write protected user ROM card is mounted in the CPU module, no data is downloaded to the flash memory or the user ROM card.  
 Notes: 2) It is possible to insert the user ROM card in the personal computer in which Loader is installed and to download the data. For the operating method, refer to the "User's Manual Loader <Reference>."

## 3-3 CPU module Specifications

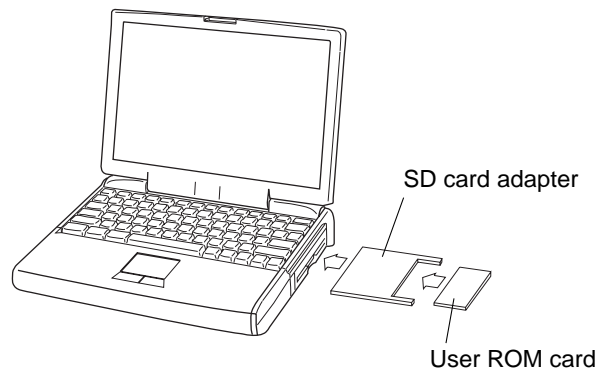
### (6) How to initialize the user ROM card

#### 1) Offline initialization

Insert the user ROM card in the SD card adapter or SD card reader/writer to make the personal computer recognize the card.

For initialization, "Memory card utility" in the "Extras" menu of loader is used. The utility executes both DOS formatting and initialization (creation of directories and files).

Note: Perform the formatting in "FAT" or "FAT32".



#### 2) Initialization by mounting a user ROM card in the CPU module

When you initialize a user ROM card that is mounted in the CPU module, be careful that only DOS formatted cards can be initialized (to create directories and files).

Insert a user ROM card in the CPU module and execute "Resource initialization" from loader when the CPU recognizes the user ROM card.

